

DB5G_J2K4 - JPEG 2000 Compression Board for IE5G_SS

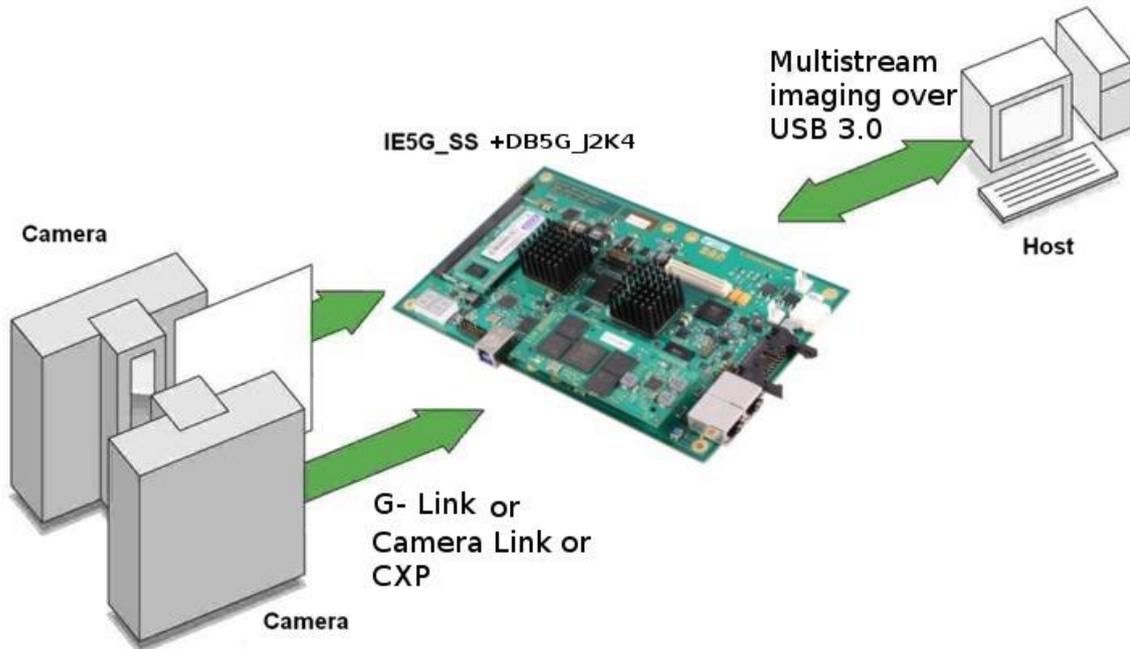


Technical description:

- Equipped with up to 4 Analog Devices ADV212 150 MHz co-processor units for fast wavelet transformation and arithmetic encoding.
- Supports lossy compression mode with 9/7 wavelet transformation.
- Supports lossless (reversible) compression mode with 5/7 wavelet transformation.
- Both wavelet transformations available in up to 6 levels of decomposition.
- Typical YUV 4:2:0 compression- 160 Mpix/s. This makes over 1000 DIN-A4 300 DPI color images per minute.
- YUV 4:4:4 lossy compression- 80 Mpix/s.
- YUV 4:4:4 lossless compression- 50 to 80 Mpix/s depending on content.
- Grayscale, subsampled color, and not-subsampled color encoding modes available.
- Maximum image size up to 16,000 x 16,000 pixels.
- Compression tile size up to 1024 x 1024 pixels for lossy and up to 512 x 512 pixels for lossless compression mode.
- Compression code block size: 64 x 64.
- Power drawn from the parent IE5G_SS image engine. No external power supply necessary.
- Manufacturing variants with 1, 2, or 4 ADV212 compression cores available, depending upon customer performance requirements.



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DB5G_J2K4 is a compression daughter board designed to connect with the image processing board IE5G_SS. These two boards combined together create an excellent combination of image processing functions. The DB5G_J2K4 daughter board allows encoding captured documents into JPEG 2000 images compressed according to the ISO/IEC 15444-1 standard. JPEG 2000 output gives smaller files at the same visual quality in comparison with standard JPEG, and it also preserves the edges of shapes even at a very deep compression level.

BAP Image Systems (BAPis) is a dependable and reliable imaging products and solution provider with highly proven industry experience. BAPis develops and manufactures cameras based not only on high speed CCD and CMOS line sensors, but also on area CMOS/CCD sensors. BAPis cameras are used in the machine vision industry as well as in the film industry. Additionally, BAPis develops and produces image grabbers and processing boards based on DSP and FPGA technologies using its own algorithms. Image processing boards are matched with camera performance and, when combined, are able to reach the highest possible throughput.

BAP Image Systems GmbH
Etzstr. 37
84030 Ergolding, Germany
Tel: +49-871-43059922
Fax: +49-871-43059929

BAP Image Systems, LLC
1120 South Freeway, Ste 214
Fort Worth, TX 76104, USA
Tel: +1-817-878-2773
Fax: +1-817-878-2739

info@bapimaging.com
www.bapimaging.com