Video Signal Transceiver PiggyBack Board


Technical description:

- full duplex serial 2 x 1200 Mbps transmission
- uses standard TLK1501 codec
- compatible with standard, low cost Ethernet CAT7 cables
- single RJ-45 bi-directional interface
- general purpose extension port interface for image processing board communication.

Advantages:

- speeds up capture of uncompressed (or losslessly compressed) images. Supports up to 120 MB/s of already processed image data.
- uses standard Ethernet CAT7 cables for image data transfer
- speeds up all transfer operations including image processing board parametrization, maintenance, calibration, etc.
- compatible with the BAP IE64_HS image processing board family.
- compatible with BAP PCI accelerator boards.
- built-in self-test and physical connection layer verification functionality

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Cat7 cable

Kamera

PCI-Glink Interface

PiggyBack

Cat7 cable
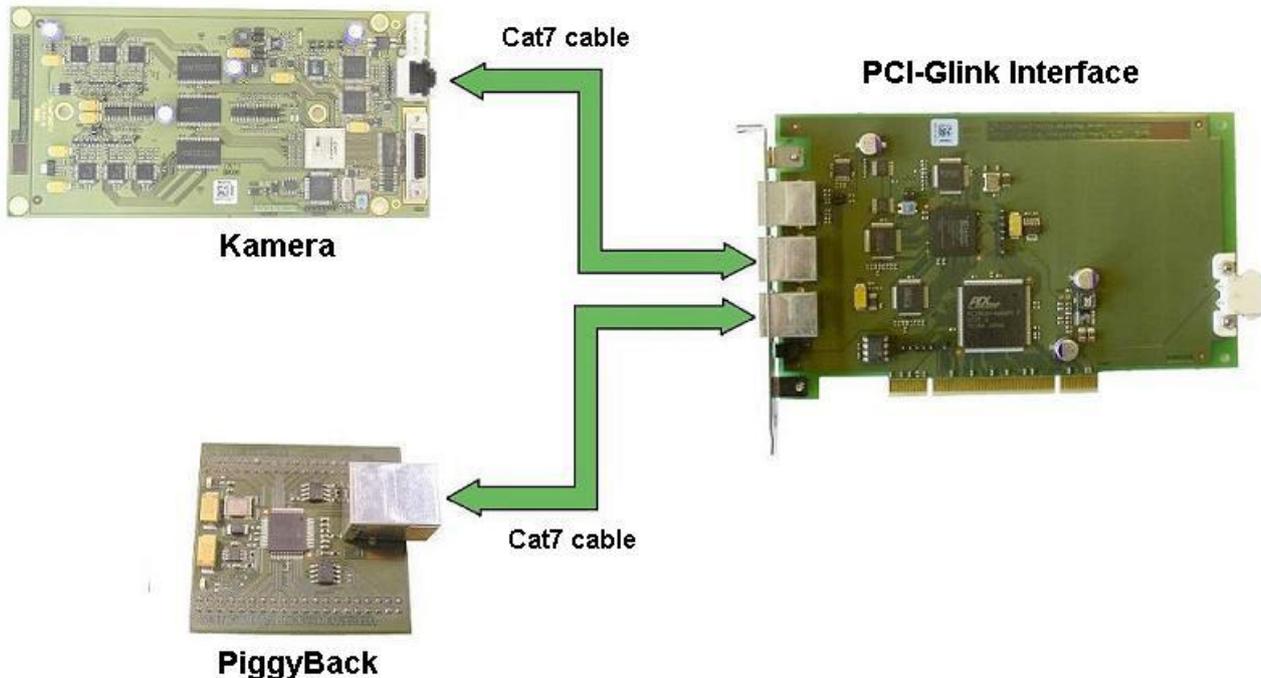
The G-Link Piggyback board solution is dedicated for transferring uncompressed images from IE64_HS family boards to a host workstation equipped with a BAP PCI transfer accelerator card. Some image processing scenarios require raw (uncompressed) images or losslessly compressed images to be transferred at a rate exceeding the USB 2.0 bandwidth. In this situation, the piggyback module together with a dedicated accelerator PCI card are able to handle a video stream of up to 120 MB/s. In this case the images are completely processed in the IE64_HS and sent to the host through a Piggyback board, CAT7 cable, and the PCI board. One PCI board can support up to three piggyback-equipped IE64_HS boards.

BAP Image Systems (BAPis) is a dependable and reliable imaging products and solution provider with highly proven industry experience. BAPis develops and manufactures cameras based not only on high speed CCD and CMOS line sensors, but also on area CMOS/CCD sensors. BAPis cameras are used in the machine vision industry as well as in the film industry. Additionally, BAPis develops and produces image grabbers and processing boards based on DSP and FPGA technologies using its own algorithms. Image processing boards are matched with camera performance and, when combined, are able to reach the highest possible throughput.